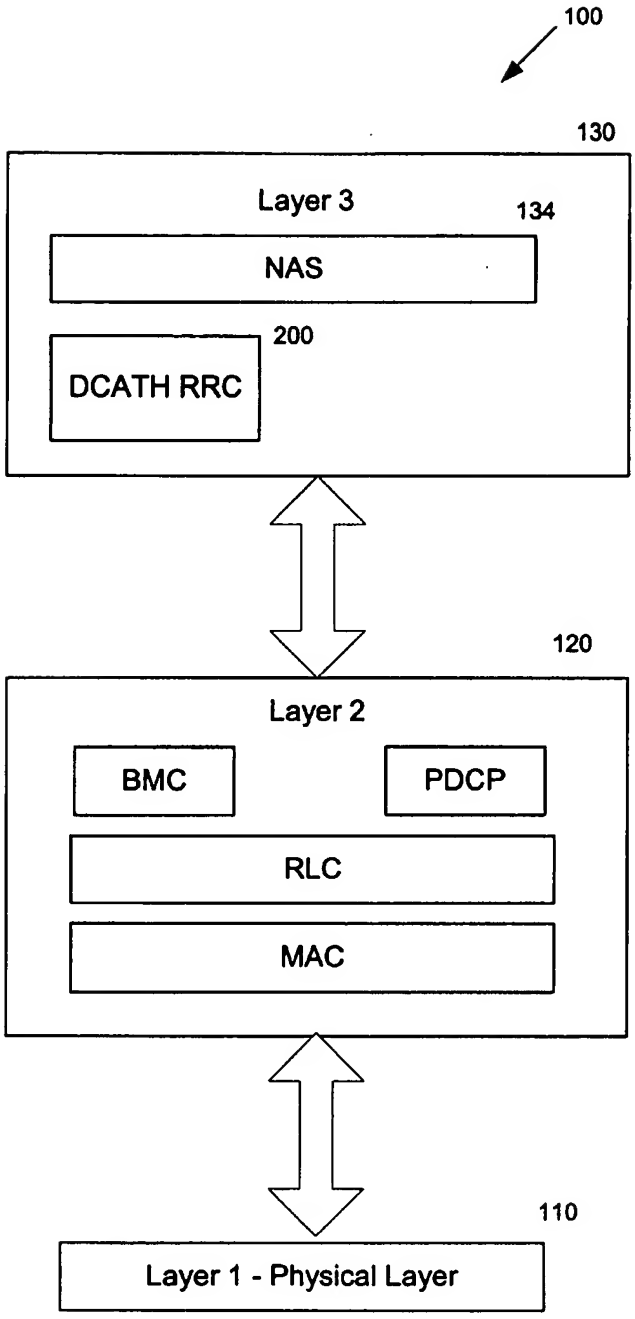

100
130
Layer 3
134
NAS
200
DCATH RRC
120
Layer 2
BMC
PDCP
RLC
MAC
110
Layer 1 - Physical Layer

Figure 1

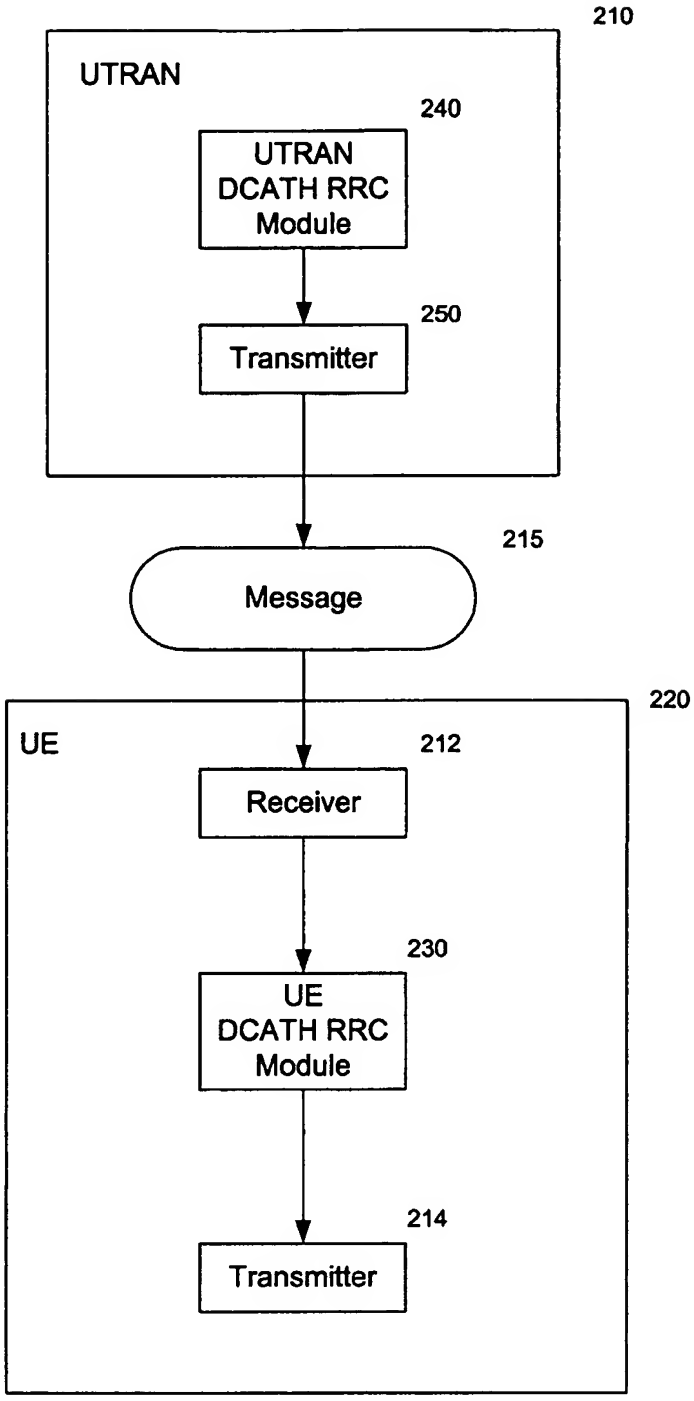

210
UTRAN
240
UTRAN DCATH RRC Module
250
Transmitter
215
Message
220
UE
212
Receiver
230
UE DCATH RRC Module
214
Transmitter

Figure 2

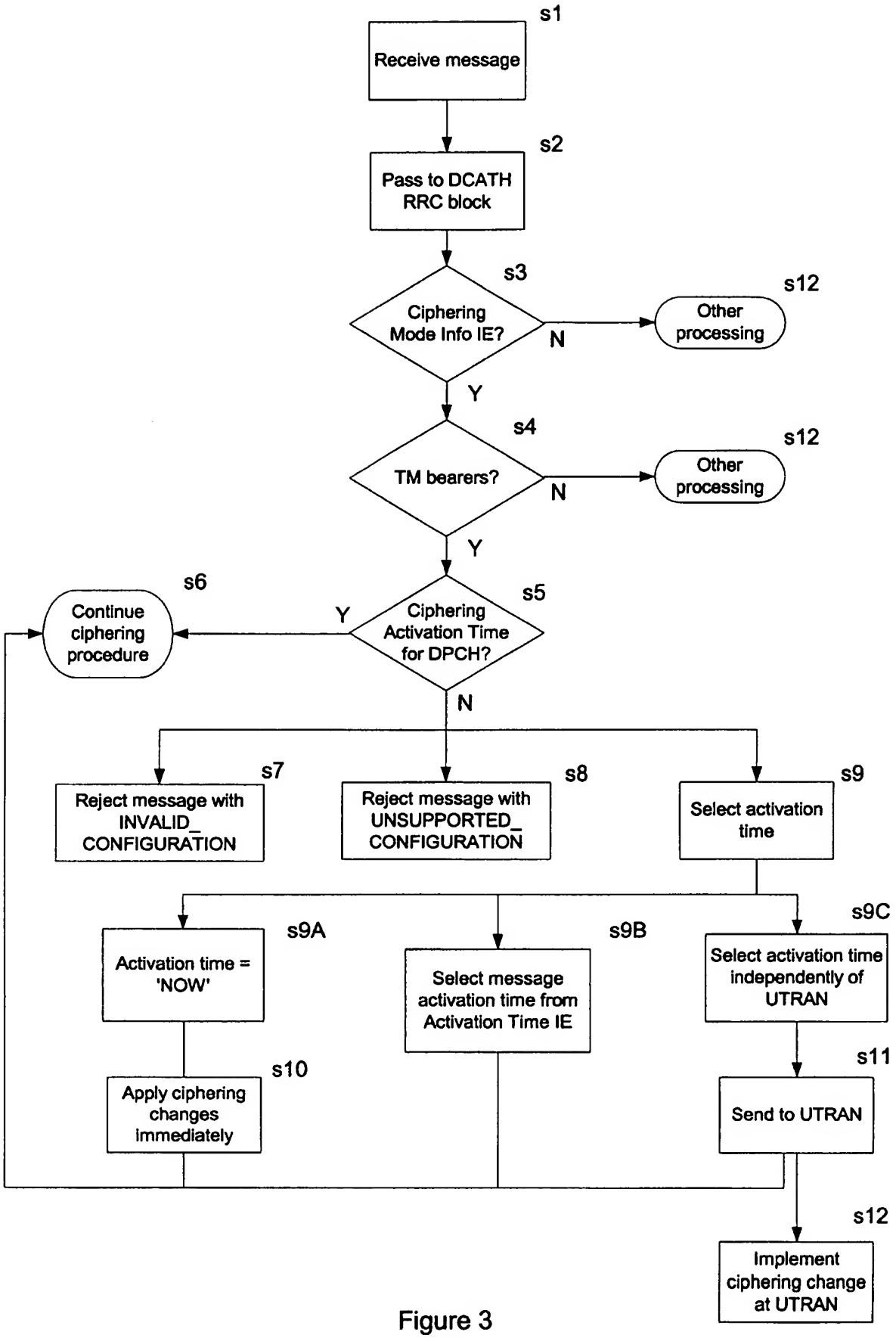

s1
Receive message
s2
Pass to DCATH RRC block
s3
Ciphering Mode Info IE?
N
s12
Other processing
Y
s4
TM bearers?
N
s12
Other processing
Y
s6
Continue ciphering procedure
Y
s5
Ciphering Activation Time for DPCH?
N
s7
Reject message with INVALID_ CONFIGURATION
s8
Reject message with UNSUPPORTED_ CONFIGURATION
s9
Select activation time
s9A
Activation time = 'NOW'
s9B
Select message activation time from Activation Time IE
s9C
Select activation time independently of UTRAN
s10
Apply ciphering changes immediately
s11
Send to UTRAN
s12
Implement ciphering change at UTRAN

Figure 3

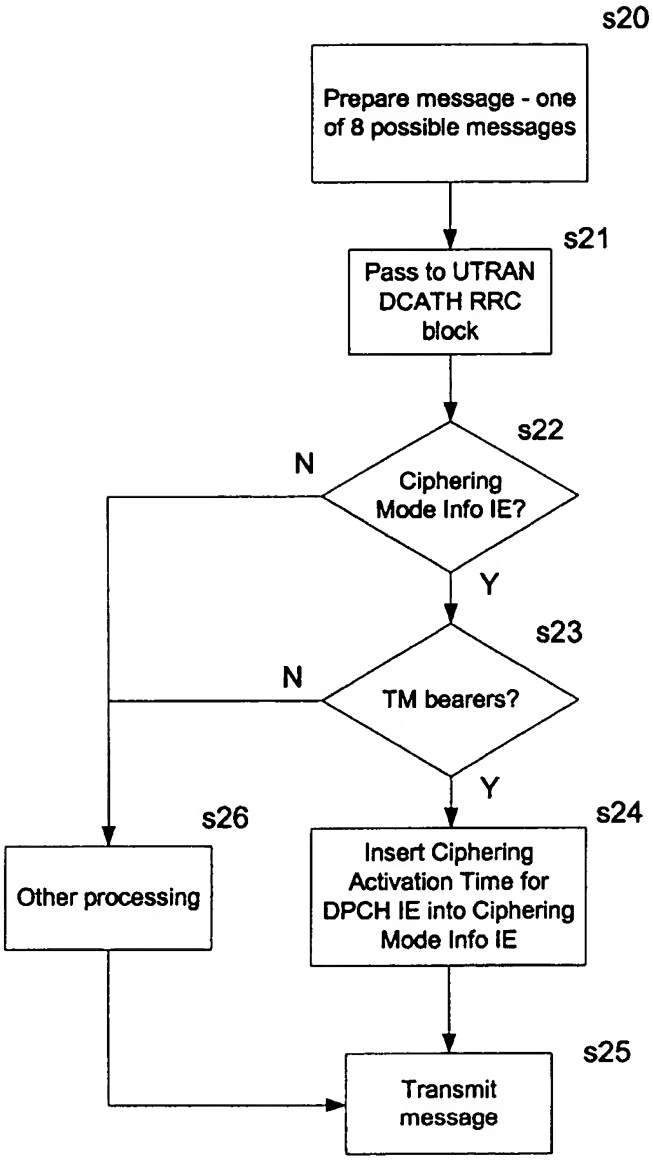
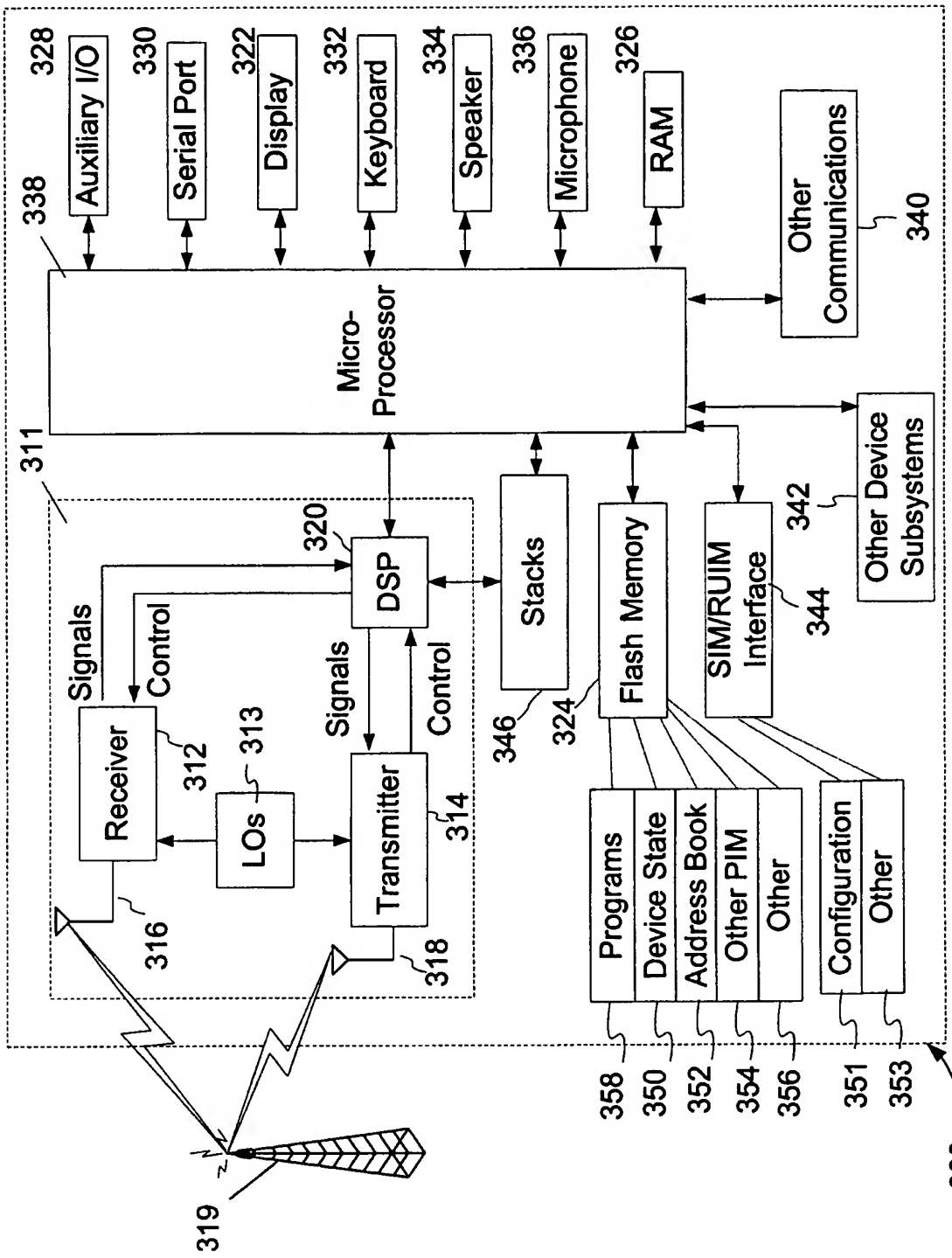

s20
Prepare message - one of 8 possible messages
s21
Pass to UTRAN DCATH RRC block
s22
Ciphering Mode Info IE?
N
Y
s23
TM bearers?
N
Y
s24
Insert Ciphering Activation Time for DPCH IE into Ciphering Mode Info IE
s25
Transmit message
s26
Other processing

Figure 4


328
Auxiliary I/O
330
Serial Port
322
Display
332
Keyboard
334
Speaker
336
Microphone
326
RAM
338
Micro-Processor
Other Communications
340
311
320
DSP
Signals
Control
Receiver
312
313
LOs
Signals
Control
Transmitter
314
316
318
319
Stacks
346
Flash Memory
324
SIM/RUIM Interface
344
342
Other Device Subsystems
Programs
Device State
Address Book
Other PIM
Other
Configuration
Other
358
350
352
354
356
351
353
300

Figur 5